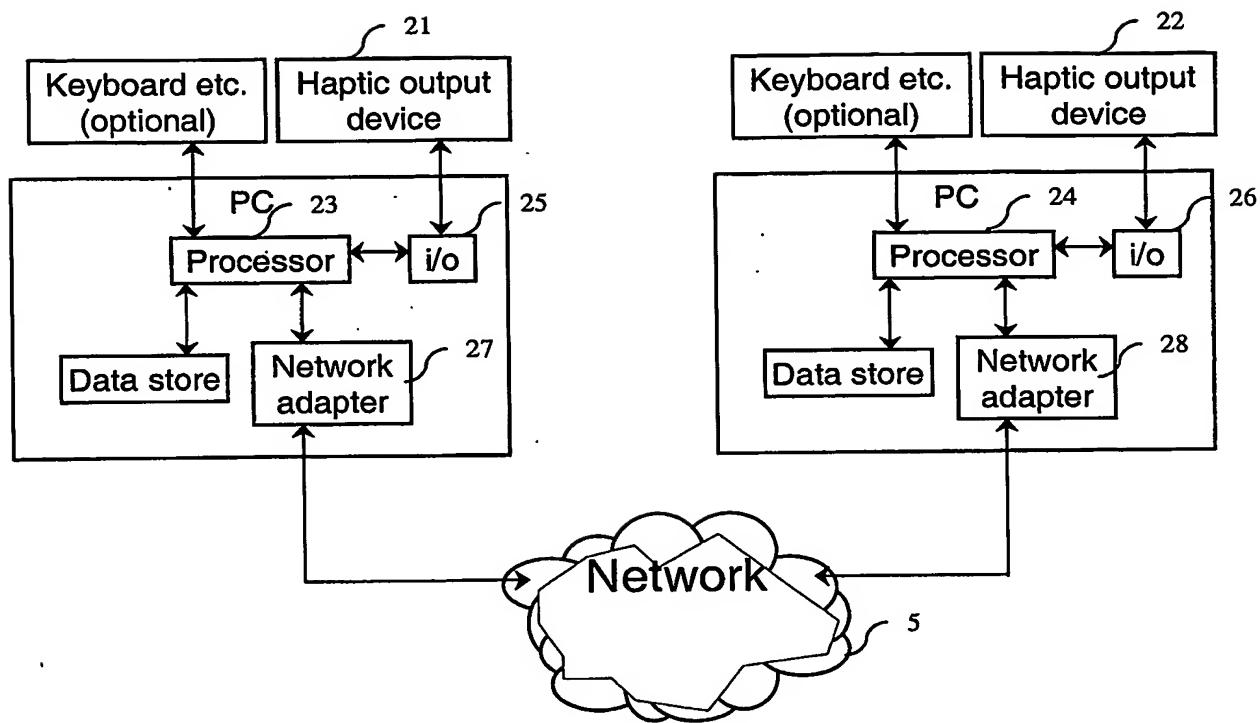


Fig 1

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Hardware

**Fig 2**

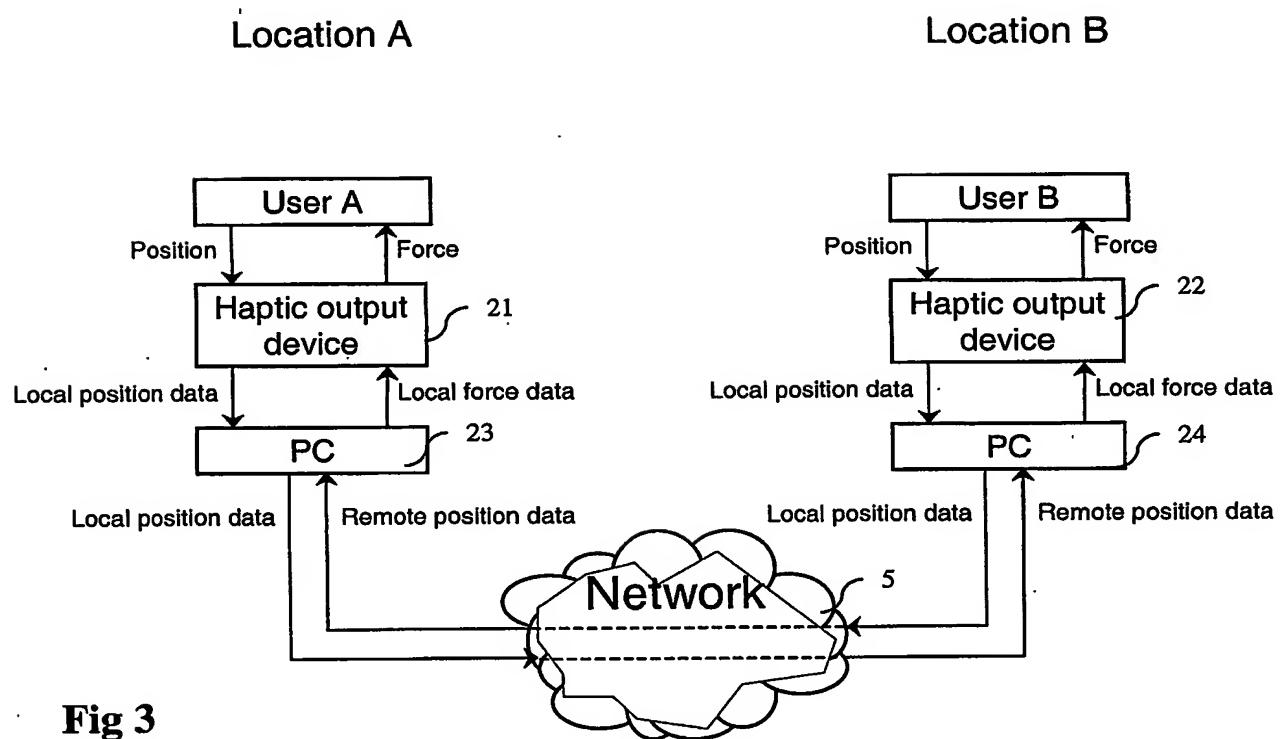


Fig 3

A Method of Latency Measurement

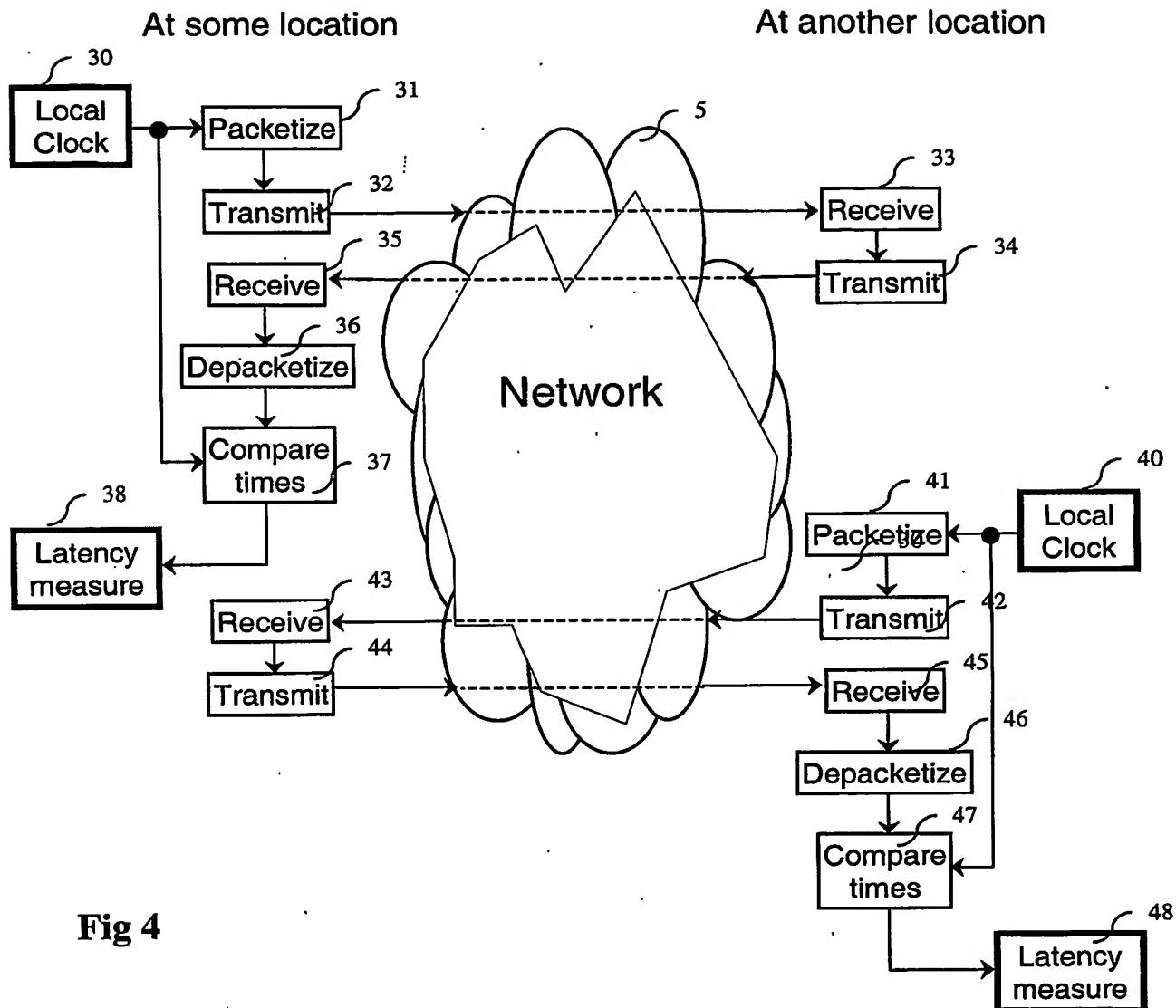
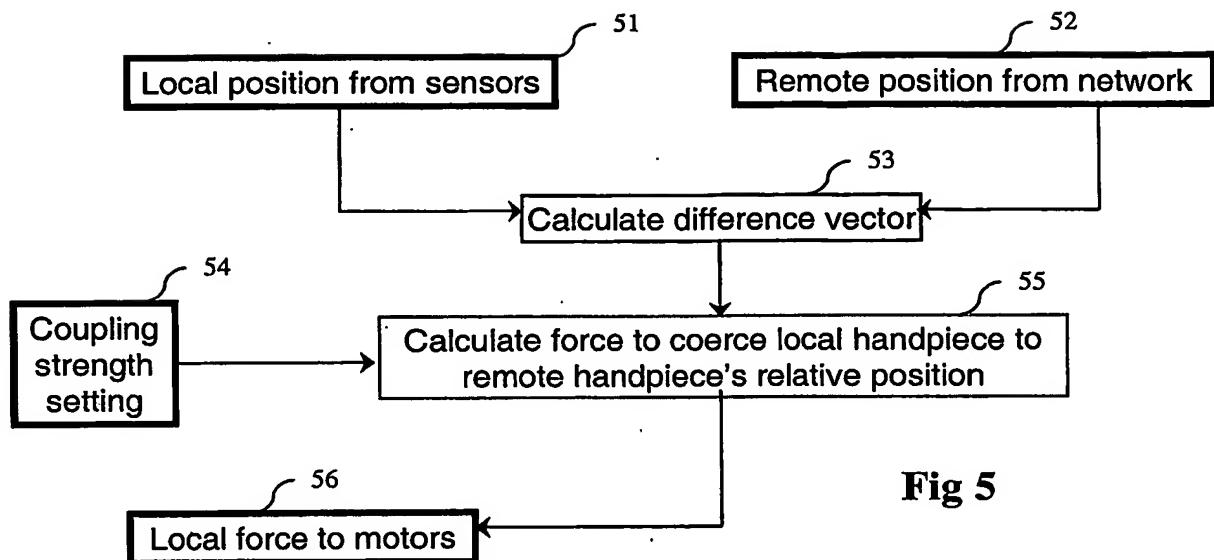
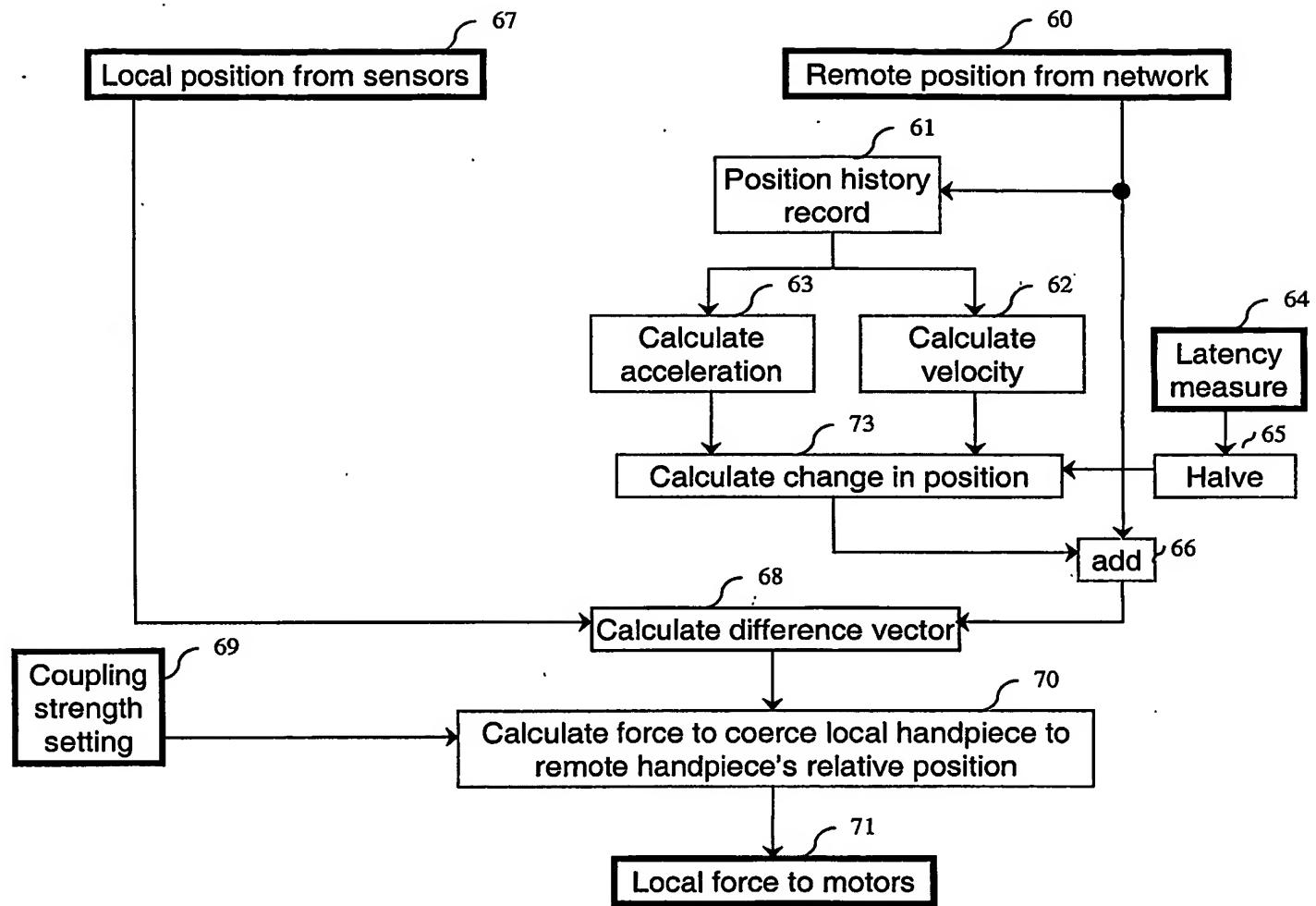


Fig 4

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**Fig 5**

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**Fig 6**

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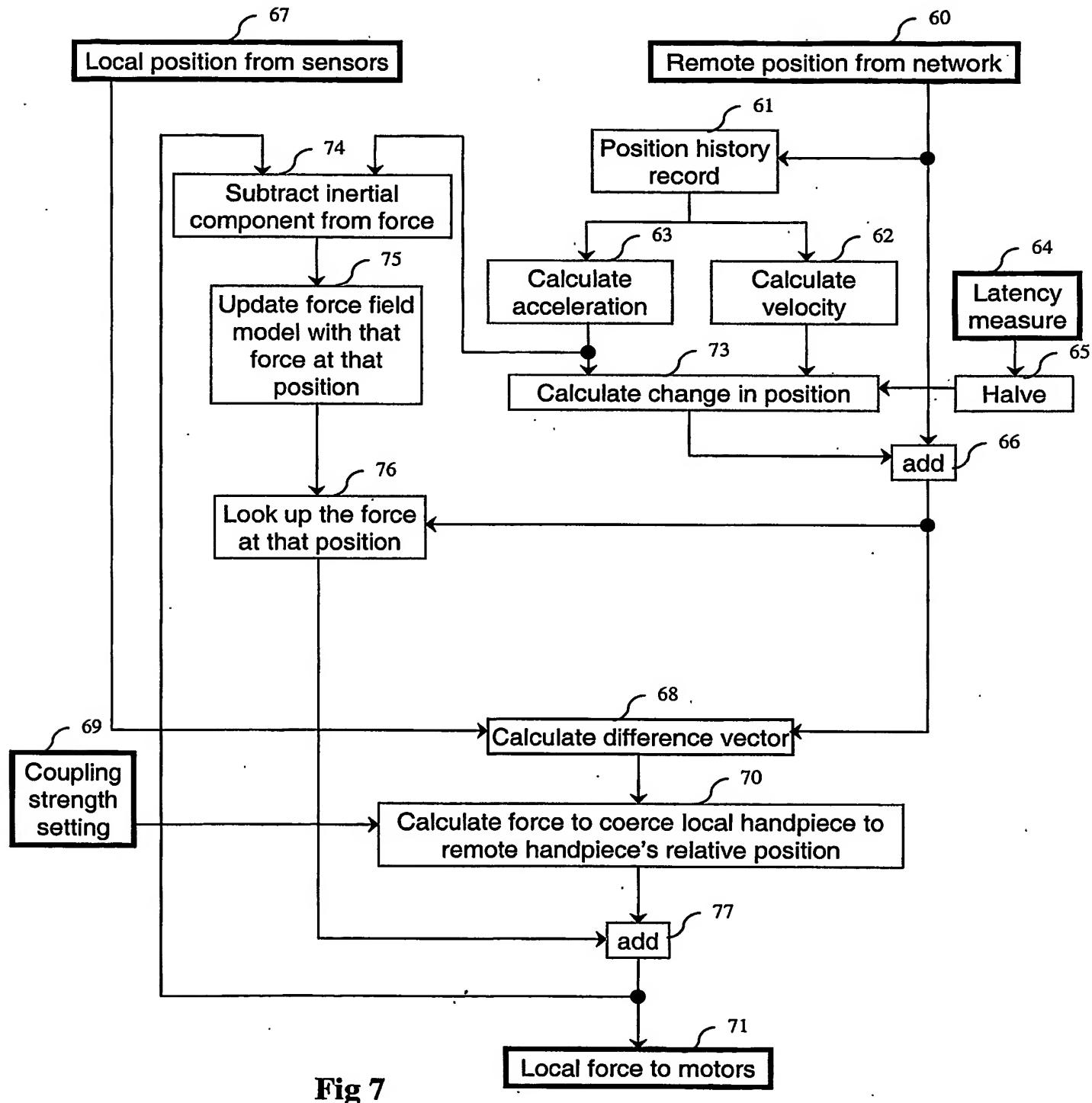


Fig 7

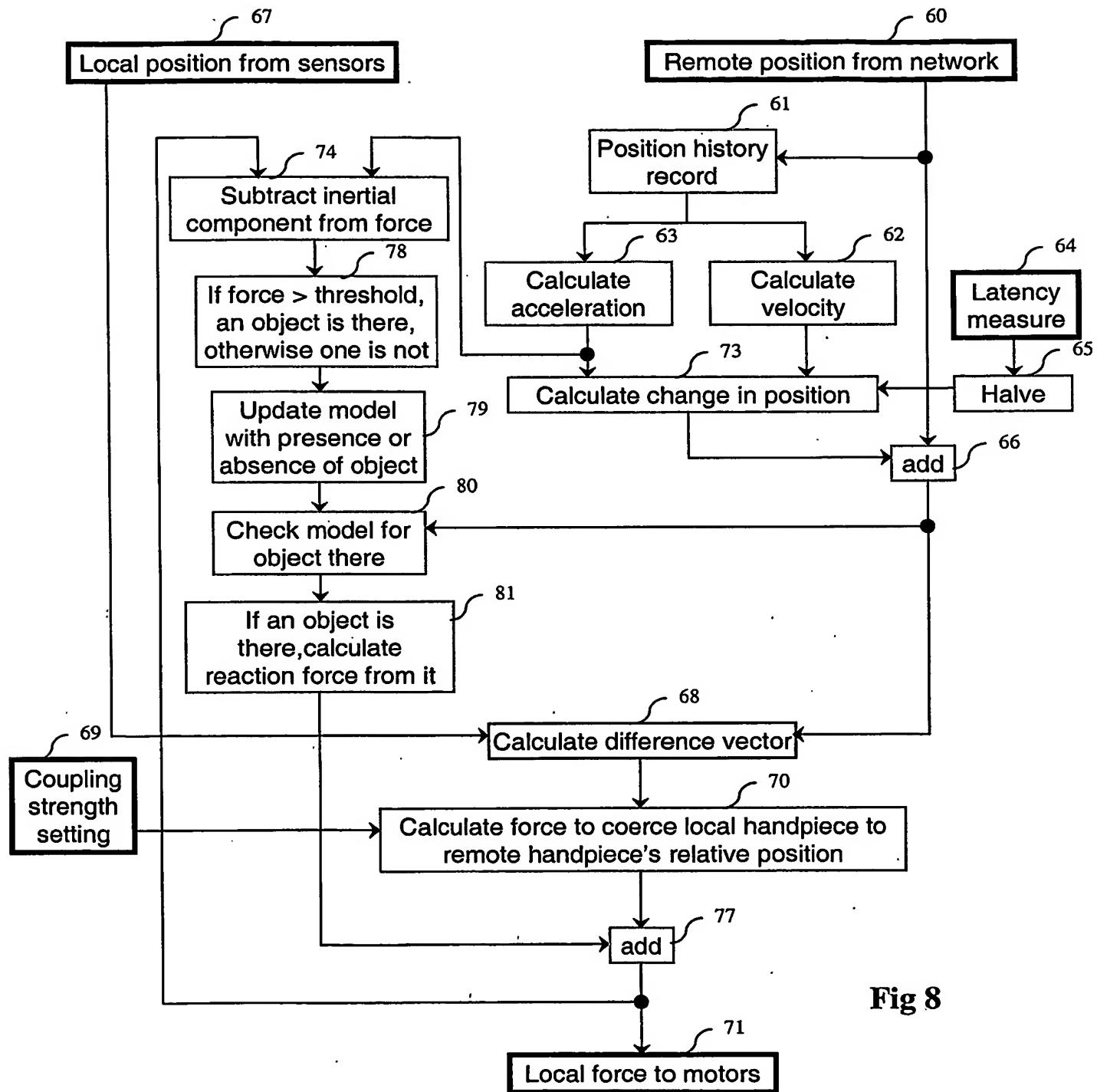


Fig 8